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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Chuen Rong Leu et al.

Assignee: Bridge Semiconductor Corporation

Title: METHOD OF MAKING A SEMICONDUCTOR CHIP
ASSEMBLY WITH AN EMBEDDED METAL PILLAR

Serial No.: 10/719,823 Filed: November 21, 2003

Examiner: Unknown Group Art Unit: 2812

Atty. Docket No.: BDG024-1

COMMISSIONER FOR PATENTS
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Alexandria, VA 22313-1450

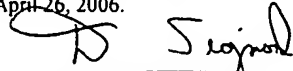
REQUEST FOR CORRECTED FILING RECEIPT

Attached is a copy of the official filing receipt received from the Patent and Trademark Office in the captioned-application for which a corrected filing receipt is respectfully requested.

The Title should be "METHOD OF MAKING A SEMICONDUCTOR CHIP ASSEMBLY WITH AN EMBEDDED METAL PILLAR" (rather than "SEMICONDUCTOR CHIP ASSEMBLY WITH EMBEDDED METAL PILLAR") as set forth in the Transmittal and the Preliminary Amendment.

The correction is noted in red ink on the attached copy of the filing receipt.

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on April 26, 2006.

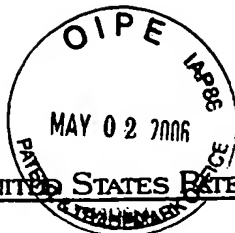


David M. Sigmond
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4, 26, 06
Date of Signature

Respectfully submitted,

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UNITED STATES DEPARTMENT OF COMMERCE
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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/719,823	11/21/2003	2812	2349	BDG024-1	36	200	11

CONFIRMATION NO. 2973

David M. Sigmond
 2440 Andrew Drive
 Superior, CO 80027

FILING RECEIPT



OC000000011945414

Date Mailed: 02/23/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

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 Charles W.C. Lin, Singapore, SINGAPORE;

Domestic Priority data as claimed by applicant

This application is a CON of 10/714,794 11/17/2003
 which is a CIP of 10/235,331 09/05/2002 PAT 6,653,742
 which is a DIV of 09/939,140 08/24/2001 PAT 6,576,539
 which is a CIP of 09/878,626 06/11/2001 PAT 6,653,217
 which is a CIP of 09/687,619 10/13/2000 PAT 6,440,835
 and said 10/714,794 11/17/2003
 claims benefit of 60/509,299 10/07/2003
 and claims benefit of 60/507,145 09/30/2003

Foreign Applications

If Required, Foreign Filing License Granted: 02/20/2004

Projected Publication Date: Request for Non-Publication Acknowledged

Non-Publication Request: Yes

Early Publication Request: No

** SMALL ENTITY **

Title *Method of making a*
 Semiconductor chip assembly with embedded metal pillar
 Preliminary Class 438
an

**LICENSE FOR FOREIGN FILING UNDER
 Title 35, United States Code, Section 184
 Title 37, Code of Federal Regulations, 5.11 & 5.15**

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